



JAN 29 2004

TRADEMARK OFFICE

FIG. 1 (BACKGROUND ART)

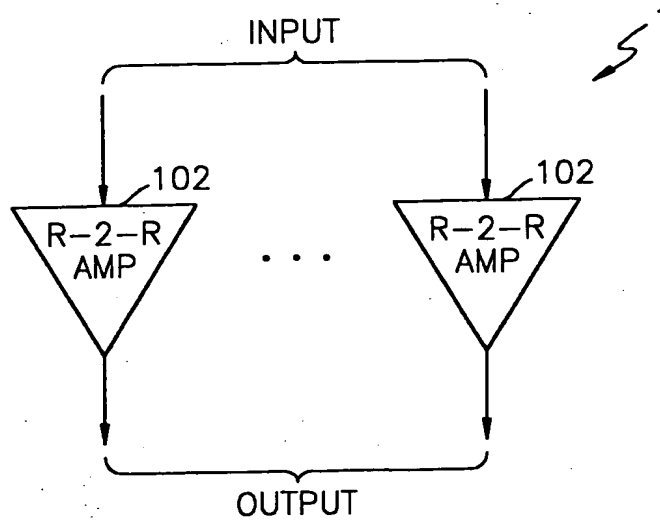
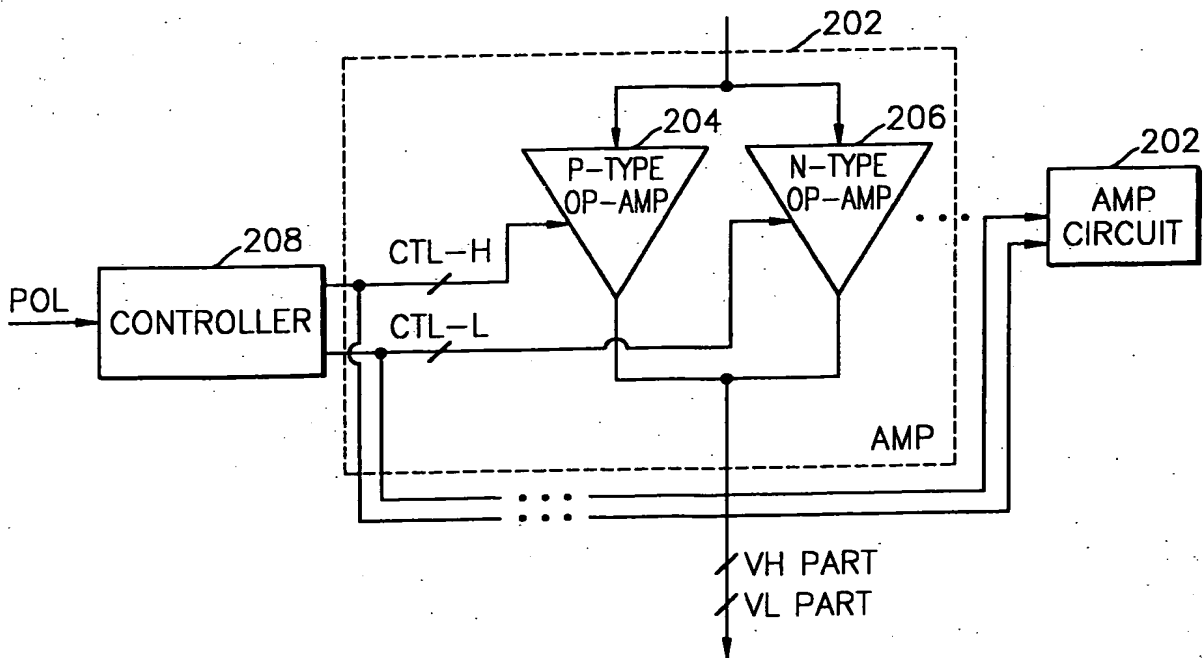


FIG. 2 (BACKGROUND ART)



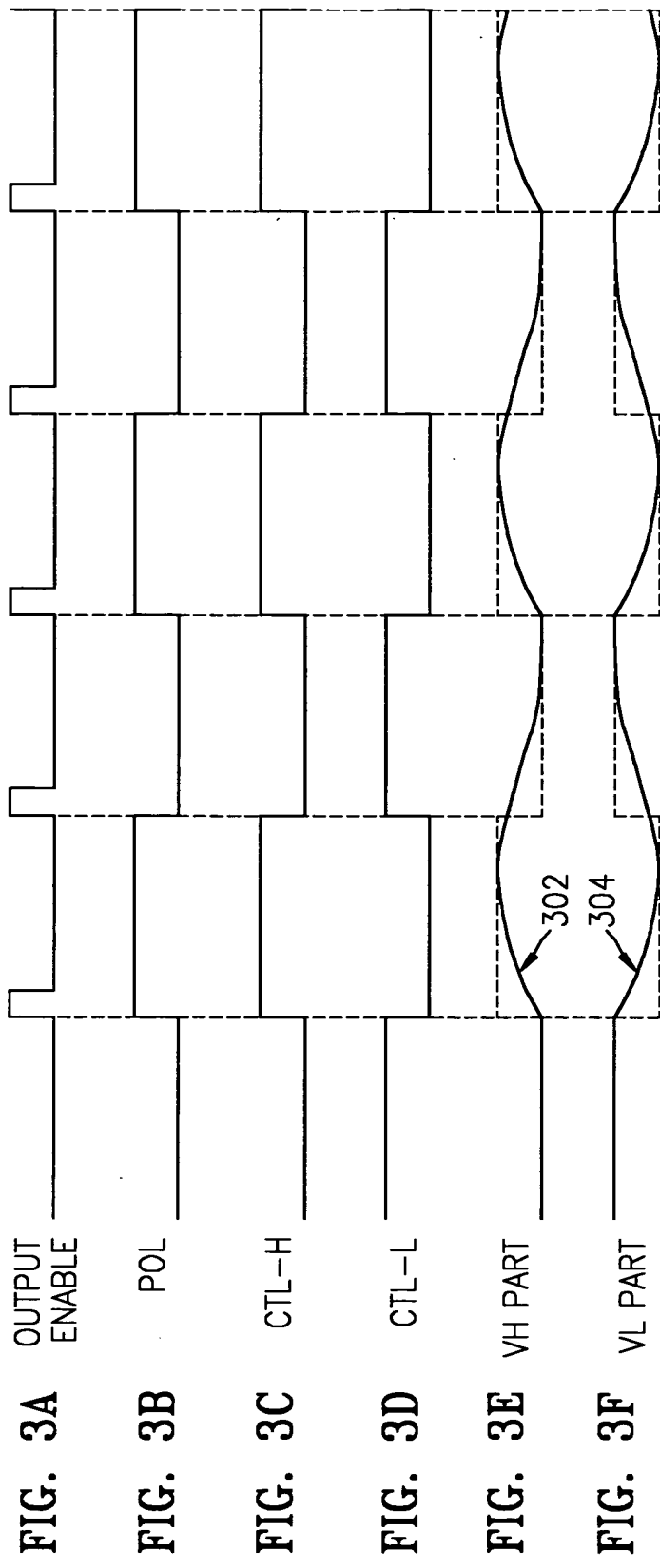


FIG. 4

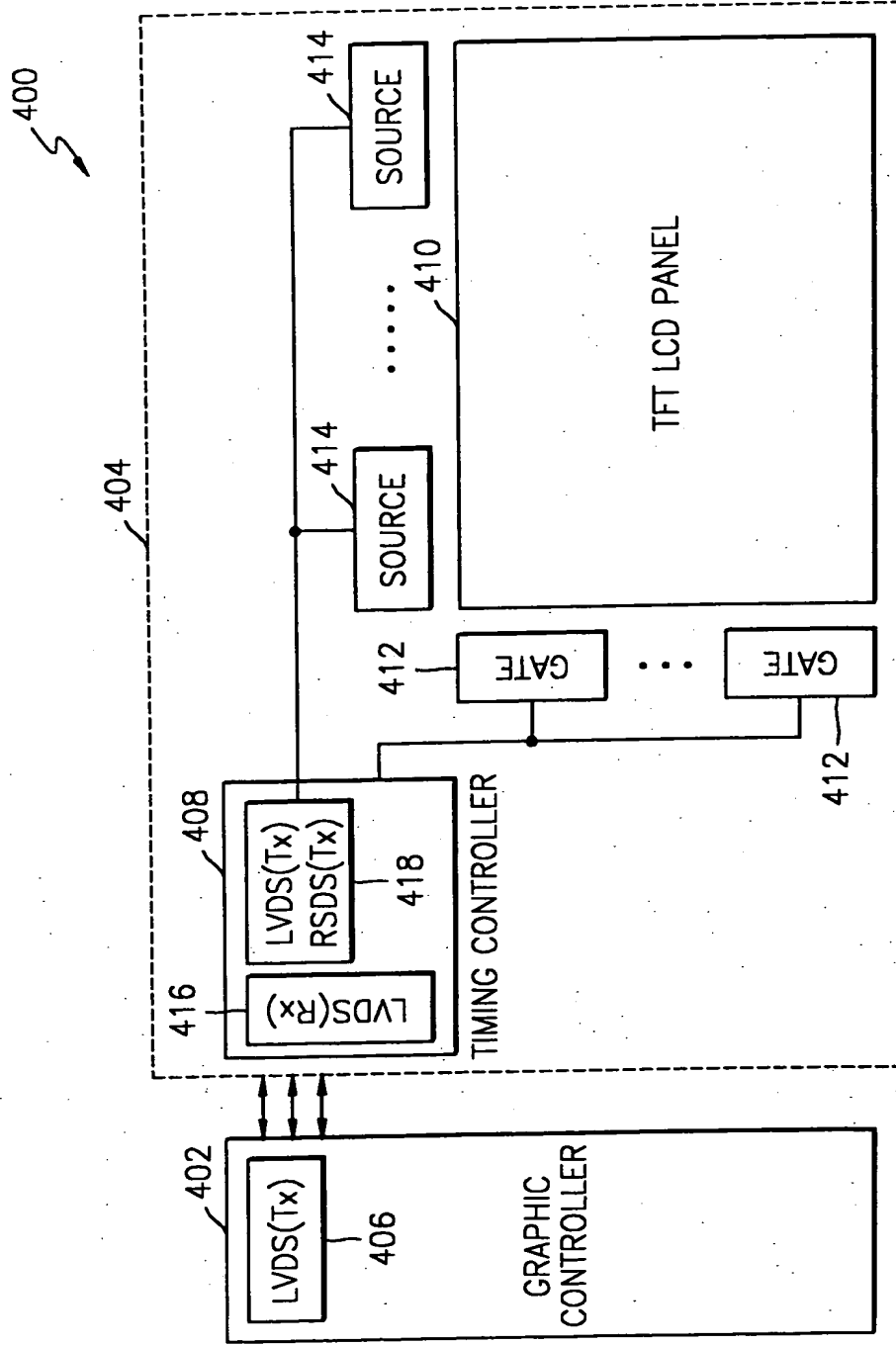
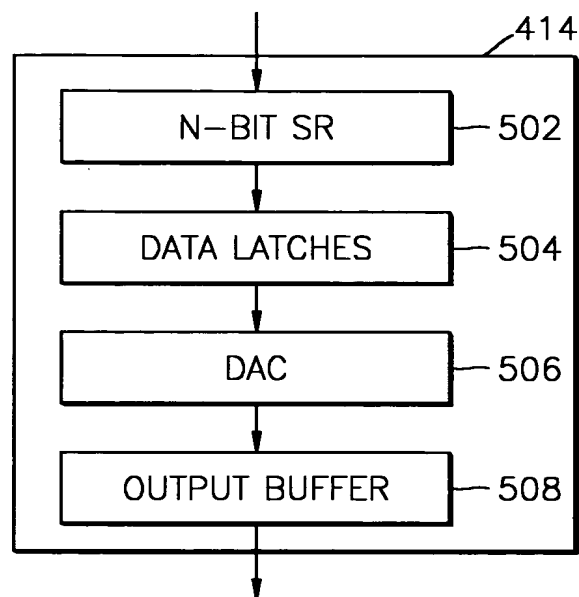


FIG. 5

FROM TIMING CONTROLLER 408



TO LCD PANNEL 410

FIG. 6

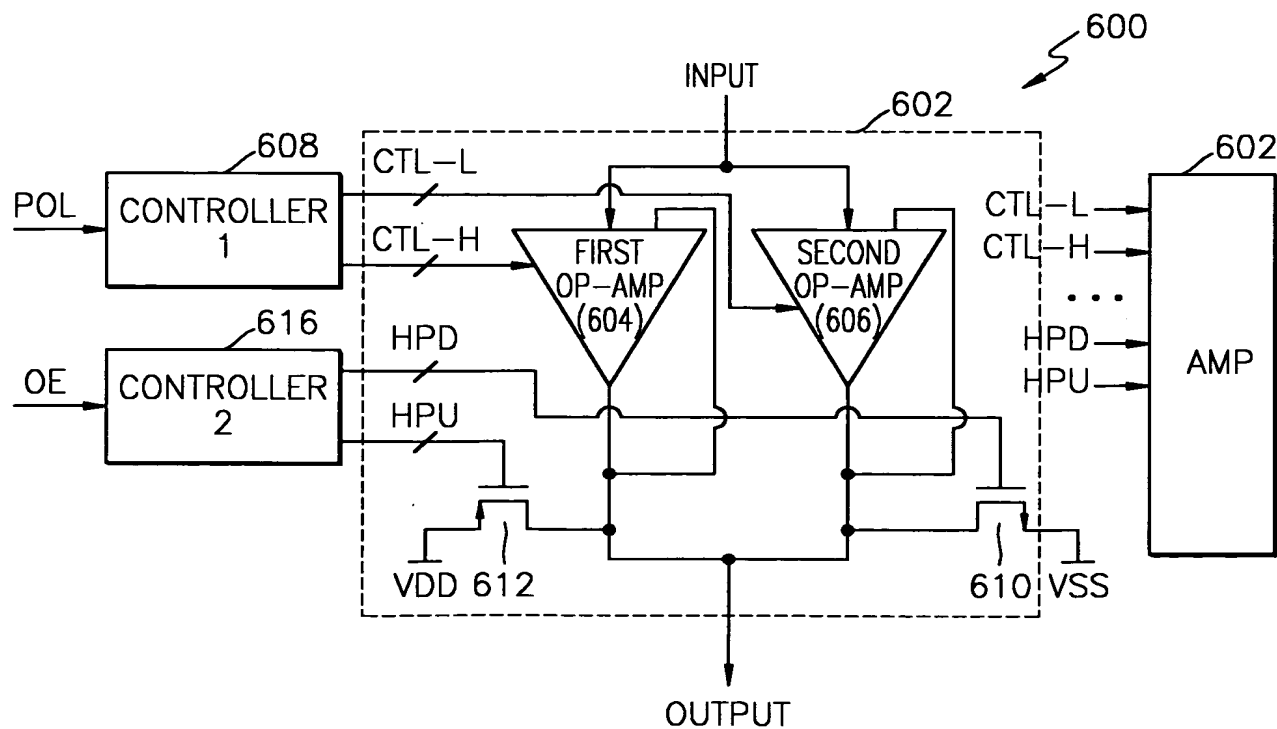


FIG. 7

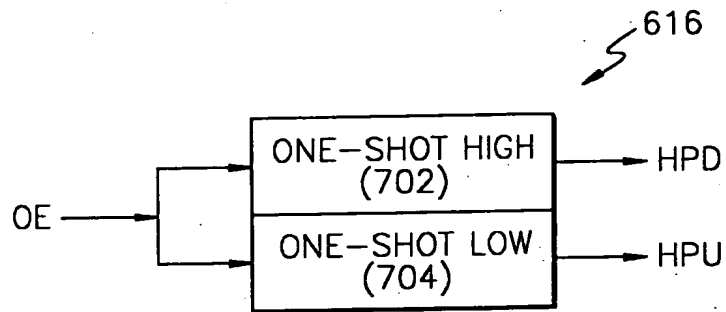


FIG. 8A

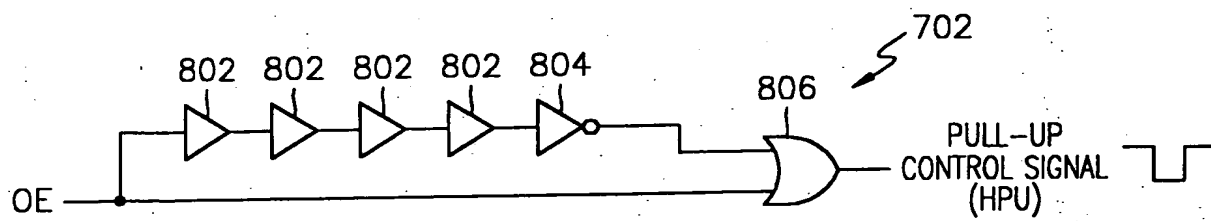


FIG. 8B

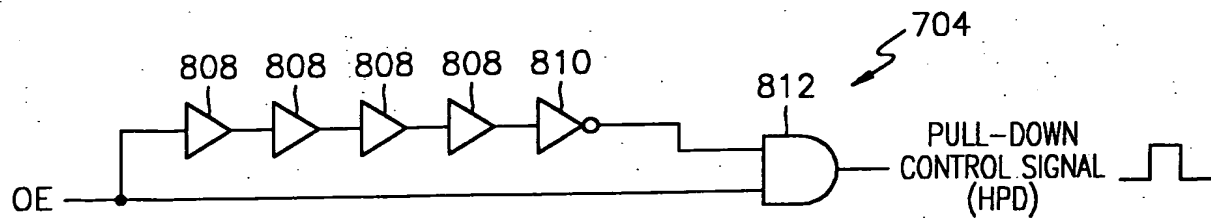


FIG. 9A. OUTPUT ENABLE.

FIG. 9B POL

FIG. 9C CTL-H

FIG. 9D CTL-L

FIG. 9E HPU

FIG. 9F

FIG. 9G

FIG. 9H

FIG. 10A

